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(54) Title: RECONFIGURABLE PARALLELISM ARCHITECTURE

(57) Abstract: Method and apparatus to perform reconfigurable parallel processing are described.



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# INTERNATIONAL SEARCH REPORT

International application No.

PCT/US05/09390

## A. CLASSIFICATION OF SUBJECT MATTER

IPC: G06F 15/00( 2006.01)

USPC: 712/015

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 712/015712/20,21,22

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
Please See Continuation Sheet

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,212,777 A (Gove et al.) 18 May 1993, see entire document.	1-24
A	US 5,475,856 A (Kogge) 12 December 1995.	1-24



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:		"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A"	document defining the general state of the art which is not considered to be of particular relevance	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
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"P"	document published prior to the international filing date but later than the priority date claimed		

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Continuation of B. FIELDS SEARCHED Item 3:

USPAT;US-PGPUB;EPO;JPO;IBM\_TDB

SIMMD,MIMD,SIMD,configur\$5,switch\$3,mesh\$3,control\$4,process\$3